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In this paper, we present a novel ultra-low voltage (ULV) operational transconductance amplifier (OTA) topology inspired by the DIG-OTA. The proposed amplifier.

This paper introduces an inverter-based OTA designed in a 65 nm CMOS technology, showcasing exceptionally low power consumption and an extremely low supply ...

Abstract: This work presents a single-stage, inverter-based, pseudo-di erential amplifier that can work with ultra-low supply voltages.

In this paper we demonstrate and analyse how the differential ultra low voltage inverter can be designed in order to achieve the most beneficial conditions concerning speed, stability and EDP.

By ensuring that every CMOS inverter from a standard-cell library operates with a well-defined quiescent current and output voltage, the suggested method makes it possible to ...

This work presents a single-stage, inverter-based, pseudo-differential amplifier that can work with ultra-low supply voltages. A novel common-mode stabilization loop allows proper differential ...

Several examples of pseudo-differential, inverter-based amplifiers with different circuits for the stabilization of the CM output voltage have been presented in the literature.

In this work a novel technique to design ultra-low voltage (ULV), ultra-low power (ULP), inverter-based OTAs is presented. The proposal consists in utilizing a

This work aims to discuss the challenges of implementing an integrated ultra low voltage start-up clock/oscillator, the state of the art and propose four new variants of a body ...

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